

1734
In

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: OHASHI, Hitoshi et al.

Group Art Unit: 1734

Serial No.: 10/047,992

Examiner: G. R. KOCH

Filed: January 17, 2002

P.T.O. Confirmation No.: 5363

For: METHOD AND APPARATUS FOR FORMING ELECTRIC CIRCUIT

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.56

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 28, 2005


Sir:

In compliance with applicants duty of disclosure under 37 C.F.R. 1.56, enclosed herewith is a copy of the European Search Report along with copies of the references cited therein. The Search Report was cited during the prosecution of the corresponding European patent application.

In the event that any fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

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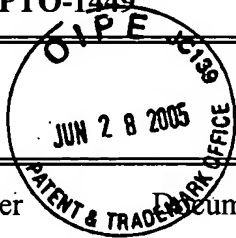


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Enclosures: PTO-1449, Search Report, References (6)

INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 020052	Serial No. 10/047,992
	Applicant(s): OHASHI, Hitoshi et al.	
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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS

_____	AK	Copy of European Search Report dated June 1, 2005 (4 pages).
_____	AL	Patent Abstracts of Japan, vol. 1999, no. 14, December 22, 1999 & JP 11 261193 A (SINTOKOGIO LTD.), September 24, 1999.
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Examiner		Date Considered